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## EDUCATION

<b>2014</b>	<i>PhD, University of New South Wales &amp; National ICT Australia</i>  “Leakage in Trustworthy Systems”, supervisor Gernot Heiser, software systems research group (SSRG).
<b>2005–2009</b>	<i>PhD-track Research Engineer, National ICT Australia</i>  L4.verified & seL4 projects, supervisor Gerwin Klein.
<b>2004</b>	<i>BSc (Hons), University of New South Wales</i>  Mathematics & Computer Science, major in Systems and Algebra.

## RESEARCH EXPERIENCE

The L4.verified project<sup>1</sup> produced the first full correctness proof of a general-purpose operating system (micro-)kernel: seL4<sup>2</sup> (Derrin et al., 2006; Klein et al., 2009, 2010). I was the principal author of the final implementation, which achieved the fastest IPC (inter-process communication) operations ever published on the ARM architecture. The nondeterministic, monadic refinement framework used for the proof was described in Cock et al. (2008).

Roughly 10% of both the C code of seL4, and of the lines of proof script dealt with packed structure manipulation, and were automatically generated (Winwood et al., 2009), using a custom DSL (domain-specific language) compiler (Cock, 2008), which co-generates Isabelle/HOL proof script.

My high-performance, retargetable CPU/system simulator, Lyrebird (Cock, 2010), provided a prototyping platform with an automatically-generated formal model.

My thesis (Cock, 2014a) dealt with the detection and mitigation of covert and side channels in component systems. We developed techniques for detecting, modelling and mitigating compromising channels in security-critical software, in particular both the empirical evaluation of hardware-based channel capacity, and the formal verification of probabilistic security properties (Cock, 2011, 2013, 2014b).

My published formalisation of the probabilistic programming logic/refinement framework pGCL (Cock, 2012) was used to machine check these proofs, and has since been accepted to the archive of formal proofs (Cock, 2014c).

## TEACHING EXPERIENCE

<sup>1</sup><http://www.ertos.nicta.com.au/research/l4.verified/>

<sup>2</sup><http://ssrg.nicta.com.au/projects/seL4/>

<b>2009–2014</b>	<p><i>Tutor, Operating Systems and Advanced Operating Systems courses, UNSW</i></p> <p>Small-group instruction of both undergraduate and postgraduate students, and marking duties. Individual guidance and assessment for advanced student projects.</p>
<b>2002–2004</b>	<p><i>Consultant &amp; Replacement Tutor, Higher Computing 1A, UNSW</i></p> <p>Individual tuition and guidance for first-year undergraduate students, and supervision of advanced student projects.</p>

## INDUSTRIAL EXPERIENCE

<b>2005–2014</b>	<p><i>Computer Support Officer, National ICT Australia.</i></p> <p>Technical and performance-optimisation support for the automated regression testing of large mechanised proofs. Resource planning and equipment acquisition.</p>
<b>2003–2005</b>	<p><i>Programmer, Brain Resource Pty. Ltd.<sup>a</sup></i></p> <p>Developed a clinical electroencephalogram acquisition system. Development was primarily in Python and C, on a customised Debian distribution. This system was deployed worldwide.</p> <p>This system included a customised low-latency audio driver, with experimentally-verified jitter bounds.</p> <hr/> <p><sup>a</sup><a href="http://www.brainresource.com/">http://www.brainresource.com/</a></p>

## SEMINARS & PRESENTATIONS

<b>2014</b>	<ul style="list-style-type: none"> <li>• “From probabilistic operational semantics to information theory”, 5th Conference on Interactive Theorem Proving, Vienna, Austria.</li> <li>• “How to navigate the literature”, NICTA SSRG PhD student boot camp, UNSW, Sydney, Australia.</li> <li>• “Measuring and Mitigating Side Channels”, NICTA software systems summer school, Sydney, Australia.</li> </ul>
<b>2013</b>	<ul style="list-style-type: none"> <li>• “Practical Probability — Applying pGCL to Lattice Scheduling”, 4th Conference on Interactive Theorem Proving, Rennes, France.</li> <li>• “Measuring and Mitigating Side Channels”, Systems Group, ETH, Zürich, Switzerland.</li> <li>• “Measuring and Mitigating Side Channels”, Dependable Systems Lab, EPFL, Lausanne, Switzerland.</li> <li>• “Lyrebird — A Retrospective”, Cambridge Computer Laboratory, Cambridge, UK.</li> </ul>

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| <b>2012</b> | <ul style="list-style-type: none"> <li>• “Verifying Probabilistic Correctness in Isabelle with pGCL”, 7th Systems Software Verification Conference, Sydney, Australia.</li> </ul> |
| <b>2011</b> | <ul style="list-style-type: none"> <li>• “Exploitation as Inference”, 4th Workshop on Artificial Intelligence and Security, Chicago, USA.</li> </ul>                              |
| <b>2010</b> | <ul style="list-style-type: none"> <li>• “Lyrebird — assigning meanings to machines”, 5th Systems Software Verification Conference, Vancouver, Canada.</li> </ul>                 |

## GRANTS & AWARDS

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|------------------|---|
| <b>2009–2013</b> | <p><i>Australian Postgraduate Award</i></p> <p>A competitive federally-funded full scholarship for research students.</p>   |
| <b>2009–2013</b> | <p><i>NICTA Research Project Award</i></p> <p>A competitive scholarship for students undertaking project work at NICTA.</p> |
| <b>2009–2013</b> | <p><i>UNSW Engineering Top-Up Scholarship</i></p> <p>Limited numbers offered annually, awarded for teaching work.</p>       |
| <b>2013</b>      | <p><i>APSys 2013 Student travel grant.</i></p>  |
| <b>2009</b>      | <p><i>SOSP 2009 Student travel grant.</i></p>   |

## SCIENTIFIC ENGAGEMENT

### JOURNAL REVIEWING

- Elsevier—Science of Computer Programming

### CONFERENCE/WORKSHOP REVIEWING

- Springer LNCS—European Symposium on Programming, International Symposium on Formal Methods, Security Proofs for Embedded Systems, International Symposium on Automated Technology for Verification and Analysis
- USENIX—Symposium on Operating System Design and Implementation
- Elsevier—Information and Communication (GandALF 2013)
- IEEE—Real-Time and Embedded Technology and Applications Symposium

## REFERENCES

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## PUBLICATIONS

David Cock. Bitfields and tagged unions in C: Verification through automatic generation. In Bernhard Beckert and Gerwin Klein, editors, *Proceedings of the 5th International Verification Workshop*, volume 372 of *CEUR Workshop Proceedings*, pages 44–55, Sydney, Australia, August 2008.

David Cock. Lyrebird – assigning meanings to machines. In Gerwin Klein, Ralf Huuck, and Bastian Schlich, editors, *Proceedings of the 5th Systems Software Verification*, pages 1–9, Vancouver, Canada, October 2010. USENIX.

David Cock. Exploitation as an inference problem. In *Proceedings of the 4th ACM Workshop on Artificial Intelligence and Security*, pages 105–106, Chicago, IL, USA, October 2011. doi:10.1145/2046684.2046702.

David Cock. Verifying probabilistic correctness in Isabelle with pGCL. In *Proceedings of the 7th Systems Software Verification*, pages 1–10, Sydney, Australia, November 2012. doi:10.4204/EPTCS.102.15.

David Cock. Practical probability: Applying pGCL to lattice scheduling. In *Proceedings of the 4th International Conference on Interactive Theorem Proving*, pages 1–16, Rennes, France, July 2013. doi:10.1007/978-3-642-39634-2\_23.

David Cock. *Leakage in Trustworthy Systems*. Phd thesis, Computer Science and Engineering, Sydney, Australia, August 2014a.

David Cock. From probabilistic operational semantics to information theory - side channels with pGCL in isabelle. In *Proceedings of the 5th International Conference on Interactive Theorem Proving*, pages 1–15, Vienna, Austria, July 2014b. Springer.

David Cock. pGCL for Isabelle. *Archive of Formal Proofs*, July 2014c. <http://afp.sf.net/entries/pGCL.shtml>, Formal proof development.

David Cock, Gerwin Klein, and Thomas Sewell. Secure microkernels, state monads and scalable refinement. In Otmane Ait Mohamed, Csar Muoz, Sone Tahar, editor, *Proceedings of the 21st International Conference on Theorem Proving in Higher Order Logics*, pages 167–182, Montreal, Canada, August 2008. Springer. doi:10.1007/978-3-540-71067-7\_16.

Philip Derrin, Kevin Elphinstone, Gerwin Klein, David Cock, and Manuel M. T. Chakravarty. Running the manual: An approach to high-assurance microkernel development. In *Proceedings of the ACM SIGPLAN Haskell Workshop*, Portland, OR, USA, September 2006. doi:10.1145/1159842.1159850.

Gerwin Klein, Kevin Elphinstone, Gernot Heiser, June Andronick, David Cock, Philip Derrin, Dhammika Elkaduwe, Kai Engelhardt, Rafal Kolanski, Michael Norrish, Thomas Sewell, Harvey Tuch, and Simon Winwood. seL4: Formal verification of an OS kernel. In *Proceedings of the 22nd ACM Symposium on Operating Systems Principles*, pages 207–220, Big Sky, MT, USA, October 2009. ACM. doi:10.1145/1629575.1629596.

Gerwin Klein, June Andronick, Kevin Elphinstone, Gernot Heiser, David Cock, Philip Derrin, Dhammika Elkaduwe, Kai Engelhardt, Rafal Kolanski, Michael Norrish, Thomas Sewell, Harvey Tuch, and Simon Winwood. seL4: Formal verification of an operating system kernel. *Communications of the ACM*, 53(6):107–115, June 2010. doi:10.1145/1743546.1743574.

Simon Winwood, Gerwin Klein, Thomas Sewell, June Andronick, David Cock, and Michael Norrish. Mind the gap: A verification framework for low-level C. In Stefan Berghofer, Tobias Nipkow, Christian Urban, and Makarius Wenzel, editors, *Proceedings of the 22nd International Conference on Theorem Proving in Higher Order Logics*, volume 5674 of *Lecture Notes in Computer Science*, pages 500–515, Munich, Germany, August 2009. Springer-Verlag. doi:10.1007/978-3-642-03359-9\_34.